

Electronic Acknowledgement Receipt

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Title of Invention:	Method and apparatus for defect analysis of semiconductor integrated circuit
First Named Inventor/Applicant Name:	Masahiro Ishida
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Filer:	Thomas K. Scherer/Staci Harris
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1		02008071003_rtoa_20071126135125.pdf	119443 b7d8791e8940252f958b9f023e152a27bd78493c	yes	5

	Multipart Description/PDF files in .zip description		
	Document Description	Start	End
	Miscellaneous Incoming Letter	1	1
	Amendment - After Non-Final Rejection	2	2
	Applicant Arguments/Remarks Made in an Amendment	3	5
Warnings:			
Information:			
Total Files Size (in bytes):		119443	
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